

S. No.	Tool Name	Tool Description
1	EDAcloud	End-to-End Chip Design Platform, integrating Catalyzer, CircuitPro-Digital, CircuitPro-Analog, Ignitor and QuickCDC
2	Catalyzer	RTL to GDSII flow
3	CircuitPro-Digital	Digital schematic to RTL generation and Simulation. Waveform viewer also available
4	CircuitPro-Analog	Analog schematic to SPICE generation and Simulation. Waveform viewer also available
5	Ignitor	Digital Design and Verification IDE
6	QuickCDC	Clock domain crossing verification
7	uvmGEN	UVM-based verification environment generation
8	ATPGcore	ATPG pattern generation (Alpha version)